

*Amendments*

*In the Specifications:*

Please enter the following amendments.

Page 1, lines 7-15, please replace with the following:

C1

FIG. 1 shows a block diagram of a conventional thin film transistor liquid crystal display TFT-LCD. The conventional TFT-LCD includes an LCD panel 10, a data driver 20, a gate driver 30 and a timing controller 40. A plurality of gate lines (not shown), or scanning lines, are formed in parallel on the LCD panel 10, and a plurality of data lines (not shown) perpendicularly intersect the gate lines insulated from the gate lines. Further, pixel electrodes are formed at the intersection of data lines and the gate lines. A thin film transistor (TFT), which acts as a switching device, is formed at each of the pixels. A thin film transistor (TFT), which acts as a switching device, is formed at each of the pixels. A gate electrode, a source electrode and a drain electrode of the TFT is respectively connected to a gate line a data line and a pixel electrode.

Page 3, lines 6-17, please replace with the following:

C2

To achieve the above objective, the present invention provides a liquid crystal display. The LCD includes an LCD panel having a plurality of data lines, a plurality of gate lines intersecting the data lines in a substantially perpendicular manner, and a plurality of pixel electrodes arranged in a matrix configuration and each having a switch connected to one of the gate lines and one of the data lines; a gate driver for successively applying a gate voltage to the gate lines to turn on the switches; a data driver for applying

C2 a gray voltage, corresponding to image data signals, to the data lines; and a printed circuit board having a timing controller for generating both the image data signals and the shift clock signal shifting the image data signals to the data driver, a first signal wire through which the shift clock signal is transmitted, and a second signal wire through which a first clock signal with the same frequency as the shift clock signal and phase difference of 90° to 270°

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Page 3, lines 18-19, please replace with the following:

C3 According to a feature of the present invention the second signal wire is grounded with a predetermined resistance value.

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Page 7, lines 4-11, please replace with the following:

C4 Formed on the color filter substrate 110 are common electrodes (not shown) which receive a common voltage, and an R,G,B color filter layer (not shown). Formed on the TFT substrate 120 are a plurality of parallel gate lines Gn, or scanning lines, and a plurality of parallel data lines Dm that receive image signals. The gate lines Gn are laid substantially perpendicular to the data lines Dm in an insulated manner. Also, pixel electrodes are formed at corresponding areas where the data lines Dm intersect the gate lines Gn, and a thin film transistor (TFT) 125, which acts as a switching device, is formed at each of the pixels.

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Page 11, lines 19-23, please replace with the following:

C5 However, it is also possible to form the first shift clock signal CLK1 and the